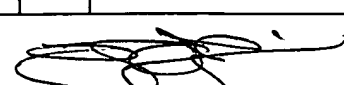


FORM PTO-1449 (SUBSTITUTE)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: GR 00 P 1031 Applicable No.: 09/760,405  Applicant: Xiaoning Nie  Filing Date: January 12, 2001 Group Art Unit: 2183			
U.S. PATENT DOCUMENTS							
EXAMINER INITIALS	A	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
B30	A	5,404,469	04/04/95	Chung et al.			
B30	B	5,941,983	08/24/99	Gupta et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
B30	J	WO 99/21088	04/29/99	WIPO			X
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	O						
	P						
EXAMINER				DATE CONSIDERED			
				1/6/04			
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STATEMENT BY APPLICANT  
(37 CFR 1.98(b))Attorney Docket No.:  
GR 00 P 1031Applic. No.  
09/760,405

Applicant

Xiaoning Nie

Filing Date  
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2183

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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	J						
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	L						
	M						
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BSC	O	Dean M. Tullsen et al.: "Simultaneous Multithreading: Maximizing On-Chip Parallelism", Proc. Of 22 <sup>nd</sup> Ann. Int. Symp. Comp. Archit., 1995, pp. 392-403;
BSC	P	Lance Hammond et al.: "A Single-Chip Multiprocessor", IEEE Computer, September 1997, pp. 79-85

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.